

C L A I M S

What is claimed is:

1. A circuitized substrate comprising:

a first layer comprised of a dielectric material including a resin material including a predetermined quantity of particles therein and not including continuous fibers, semi-continuous fibers or the like as part thereof; and

at least one circuitized layer positioned on said first layer.
2. The circuitized substrate of claim 1 wherein said first layer includes a plurality of thru-holes therein, the aspect ratio of the thickness of said circuitized substrate to the diameter of each of said thru-holes being within the range of from about 2:1 to about 20:1.
3. The circuitized substrate of claim 1 wherein said plurality of particles are comprised of silica and/or ceramic material.
4. The circuitized substrate of claim 3 wherein said particles each have a size within the range of from about 200 Angstroms to about 35 microns.
5. The circuitized substrate of claim 3 wherein said at least one circuitized layer is comprised of copper.
6. The circuitized substrate of claim 1 further including a second circuitized layer positioned on said first layer on a side opposite said at least one circuitized layer.
7. The circuitized substrate of claim 6 further including a plurality of conductive thru-holes in said first layer, selected ones of said conductive thru holes electrically coupling selected parts of said at least one circuitized layer to selected parts of said second circuitized layer.

8. The invention of claim 7 wherein said circuitized substrate comprises a chip carrier.
9. The circuitized substrate of claim 6 further including second and third dielectric layers positioned on said at least one circuitized layer and said second circuitized layer, respectively, and third and fourth circuitized layers formed on said second and third dielectric layer, respectively.
10. The circuitized substrate of claim 1 wherein said particles comprise from about 10 to about 80 percent by volume of said dielectric layer.
11. An electrical assembly comprising:

a circuitized substrate including a first layer comprised of a dielectric material including a resin material including a predetermined quantity of particles therein and not including continuous fibers, semi-continuous fibers or the like as part thereof; and

at least one circuitized layer positioned on said first layer; and

at least one electrical component positioned on and electrically coupled to said circuitized substrate.
12. The electrical assembly of claim 11 wherein said first layer of said circuitized substrate includes a plurality of thru-holes therein, the aspect ratio of thickness of said circuitized substrate to the diameter of each of said thru-holes being within the range of from about 2:1 to about 20:1.
13. The electrical assembly of claim 11 wherein said plurality of particles are comprised of silica and/or ceramic material.
14. The electrical assembly of claim 13 wherein said particles each have a size within the range of from about 200 Angstroms to about 35 microns.

15. The electrical assembly of claim 14 wherein said at least one circuitized layer is comprised of copper.
16. The electrical assembly of claim 11 further including a second circuitized layer positioned on said first layer on a side opposite said at least one circuitized layer.
17. The electrical assembly of claim 16 further including a plurality of conductive thru-holes in said first layer, selected ones of said conductive thru-holes electrically coupling selected parts of said at least one circuitized layer to selected parts of said second circuitized layer.
18. The electrical assembly of claim 17 wherein said circuitized substrate comprises a chip carrier and said at least one electrical component comprises a semiconductor chip.
19. The electrical assembly of claim 16 further including second and third dielectric layers positioned on said at least one circuitized layer and said second circuitized layer, respectively, and third and fourth circuitized layers formed on said second and third dielectric layer, respectively.
20. The electrical assembly of claim 16 wherein said particles comprise from about 10 to about 80 percent by volume of said dielectric layer.
21. A method of making a circuitized substrate, said method comprising:

providing a first layer comprised of a dielectric material including a resin material including a predetermined quantity of particles therein and not including continuous fibers, semi-continuous fibers or the like as part thereof; and

positioning at least one circuitized layer on said first layer of said dielectric material.

22. The method of claim 21 further including providing in said first layer a plurality of thru-holes, the aspect ratio of the thickness of said circuitized substrate to the diameter of each of said thru-holes being within the range of from about 2:1 to about 20:1.
23. The method of claim 22 wherein said plurality of thru-holes are provided within said first layer using a laser.
24. The method of claim 21 further including positioning a second circuitized layer on said first layer on a side opposite said at least one circuitized layer.
25. The method of claim 24 further including providing a plurality of conductive thru-holes in said first layer such that selected ones of said conductive thru holes electrically couple selected parts of said at least one circuitized layer to selected parts of said second circuitized layer.
26. The method of claim 19 further including electrically coupling at least one electrical component to said at least one circuitized layer on said first layer of said dielectric material.
27. A multilayered circuitized structure comprising:

a first circuitized substrate portion including a first layer including a dielectric material having a resin material including a predetermined quantity of particles therein and not including continuous fibers, semi-continuous fibers or the like as part thereof, and at least one circuitized layer positioned on said first layer, said circuitized substrate having a first pattern of interconnecting conductive thru-holes therein of a first density; and

second and third circuitized substrate portions positioned on opposite sides of said first circuitized substrate portion, each having a second pattern of interconnecting thru-holes therein and of a density less dense than said first density of said interconnecting conductive thru-holes of said first circuitized substrate portion, said first circuitized

substrate portion providing electrical interconnection between said second and third circuitized substrate portions.

28. The invention of claim 27 wherein said multilayered circuitized structure is a printed circuit board.
29. The invention of claim 27 wherein said multilayered circuitized structure is a chip carrier.
30. The invention of claim 29 further including at least one semiconductor chip positioned on or within said chip carrier and forming part thereof.
31. An information handling system comprising as part thereof:

a circuitized substrate including a first layer comprised of a dielectric material including a resin material including a predetermined quantity of particles therein and not including continuous fibers, semi-continuous fibers or the like as part thereof and at least one circuitized layer positioned on said first layer; and

at least one electrical component positioned on and electrically coupled to said circuitized substrate.
32. The invention of claim 31 wherein said information handling system comprises a personal computer.
33. The invention of claim 31 wherein said information handling system comprises a mainframe computer.
34. The invention of claim 31 wherein said information handling system comprises a computer server.